TOSHIBA Photocoupler GaAs Ired & Photo-Thyristor

# TLP747G

Office Machine Household Use Equipment Solid State Relay Switching Power Supply

The TOSHIBA TLP747G consists of a photo–thyristor optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP.

- Peak off-state voltage: 400 V (min.)
- Trigger LED current: 15 mA (max.)
- On-state current: 150 mA (max.)
- UL recognized: UL1577, file No. E67349
- BSI approved: BS EN60065: 1994

Certificate No. 7364 BS EN60950: 1992 Certificate No. 7365

SEMKO approved: SS4330784, certificate No. 9325163, 9522142

Isolation voltage: 4000 Vrms (min.)

Option (D4) type

VDE approved: DIN VDE0884 / 06.92,

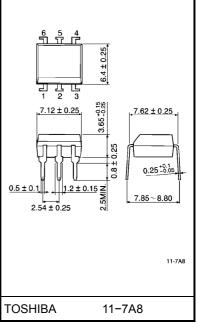
Certificate No. 74286, 91808

Maximum operating insulation voltage: 630, 890 VPK Highest permissible over voltage: 6000, 8000 VPK

When a VDE0884 approved type is needed, (Note) please designate the "option (D4)"

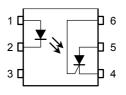
7.62mm pich 10.16mm pich  $TLP \times \times F$  type standard type Creepage distance : 7.0mm (min.) 8.0mm (min.) Clearance : 7.0mm (min.) 8.0mm (min.) Insulation thickness : 0.5mm (min.) 0.5mm (min.)

Unit in mm



Weight: 0.42 g

#### Pin Configuration (top view)



- 1: Anode
- 2: Cathode
- 3: NC
- 4: Cathode
- 5 : Anode
- 6 : Gate

#### **Maximum Ratings (Ta = 25°C)**

Characteristic		Symbol	Rating	Unit	
ΓΕD	Forward current	l <sub>F</sub>	60	mA	
	Forward current derating (Ta ≥ 39°C)	ΔI <sub>F</sub> / °C	-0.7	mA / °C	
	Peak forward current (100µs pulse, 100pps)	I <sub>FP</sub>	1	Α	
	Reverse voltage	V <sub>R</sub>	5	V	
	Junction temperature	Tj	125	°C	
	Peak forward voltage ( $R_{GK} = 27k\Omega$ )	$V_{DRM}$	400	V	
	Peak reverse voltage ( $R_{GK}$ = 27k $\Omega$ )	$V_{RRM}$	400	V	
	On-state current	I <sub>T(RMS)</sub>	150	mA	
Detector	On–state current derating (Ta ≥ 25°C)	ΔI <sub>T</sub> / °C	-2.0	mA / °C	
	Peak on-state current (100 µs pulse, 120pps)	I <sub>TP</sub>	3	Α	
	Peak one cycle surge current	I <sub>TSM</sub>	2	Α	
	Peak reverse gate voltage	$V_{GM}$	5	V	
	Power dissipation	$P_{D}$	150	mW	
	Power dissipation derating (Ta ≥ 25°C)	ΔP <sub>D</sub> / °C	-2.0	mW / °C	
	Junction temperature	Tj	100	°C	
Storag	e temperature range	T <sub>stg</sub>	-55~125	°C	
Operating temperature range		T <sub>opr</sub>	<b>−40~100</b>	°C	
Lead s	oldering temperature (10s)	T <sub>sol</sub>	260	°C	
Total p	al package power dissipation P <sub>T</sub> 250				
Total p	package power dissipation derating (Ta ≥ 25°C)	ΔP <sub>T</sub> / °C	-3.3	mW / °C	
Isolatio	on voltage (AC, 1min., R.H. ≤ 60%) (Note)	BVS	4000	Vrms	

(Note) Device considered a two terminal device: Pins 1, 2 and 3 shorted together, and pins 4, 5 and 6 shorted together.

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## **Recommended Operating Conditions**

Characteristic	Symbol	mbol Min.		Max.	Unit
Supply voltage	V <sub>AC</sub>	_	_	120	Vac
Forward current	IF	20	_	25	mA
Operating temperature	T <sub>opr</sub>	-25	_	85	°C
Gate to cathode resistance	R <sub>GK</sub>	_	27	33	kΩ
Gate to cathode capacity	C <sub>GK</sub>	_	0.01	0.1	μF

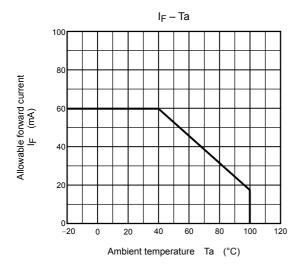
### Individual Electrical Characteristics (Ta = 25°C)

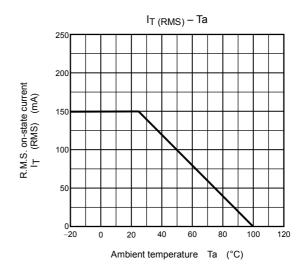
Characteristic		Symbol	Test Condition		Min.	Тур.	Max.	Unit
LED	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10mA		1.0	1.15	1.3	V
	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5V		_	_	10	μA
	Capacitance	C <sub>T</sub>	V = 0, f = 1MHz		_	30	_	pF
Detector	Off-state current	I <sub>DRM</sub>	V <sub>AK</sub> = 400V R <sub>GK</sub> = 27kΩ	Ta = 25°C	_	10	5000	nA
				Ta = 100°C	_	1	100	μA
	Reverse current	lanu	V <sub>KA</sub> = 400V	Ta = 25°C	_	10	5000	nA
		I <sub>RRM</sub>	$R_{GK} = 27k\Omega$	Ta = 100°C	_	1	100	μA
	On–state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 100mA		_	0.9	1.3	V
	Holding current	lΗ	R <sub>GK</sub> = 27kΩ		_	0.2	_	mA
	Off–state dv / dt	dv / dt	$V_{AK} = 280V, R_{GK} = 27k\Omega$		5	10	_	V / µs
	Capacitance $C_j$ $V = 0, f = 1MH$	0	V = 0,	Anode to gate	_	20	_	,r
		f = 1MHz	Gate to cathode	_	350	_	pF	

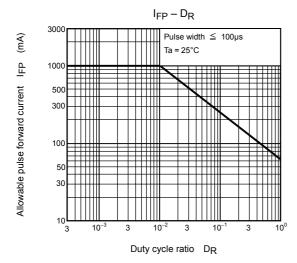
### Coupled Characteristics (Ta = 25°C)

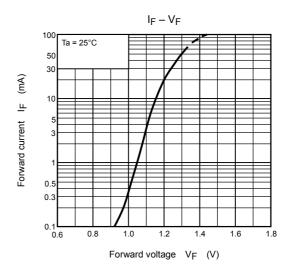
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I <sub>FT</sub>	$V_{AK}$ = 6V, $R_{GK}$ = 27k $\Omega$	_	_	15	mA
Turn-on time	t <sub>on</sub>	$I_F = 30$ mA, $V_{AA} = 50$ V R <sub>GK</sub> = 27k $\Omega$	_	10	-	μs
Coupled dv / dt	dv / dt	$V_S = 500V, R_{GK} = 27k\Omega$	500	_	_	V / µs
Capacitance (input to output)	Cs	V <sub>S</sub> = 0, f = 1MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500V, R.H. ≤ 60%	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	4000	_	_	Vrms
Isolation voltage		AC, 1 second, in oil	_	10000	_	VIIIIS
		DC, 1 minute, in oil	_	10000	_	Vdc

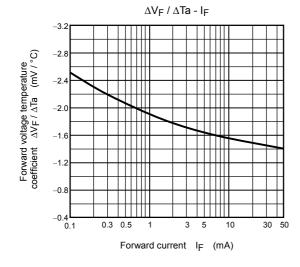
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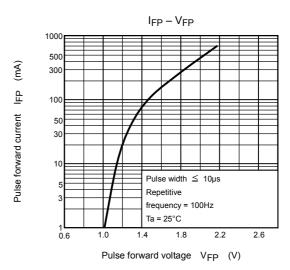




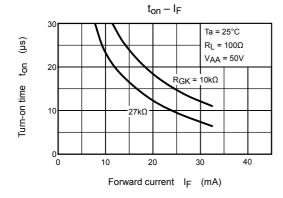


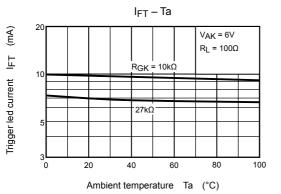


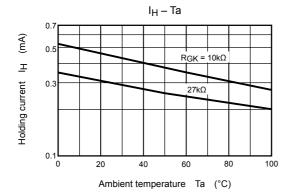


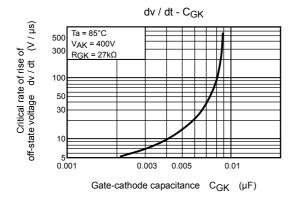


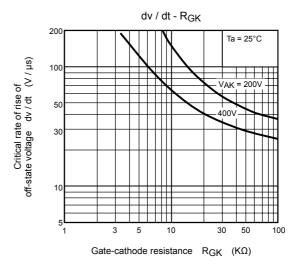
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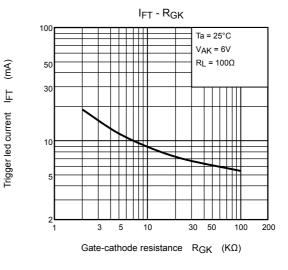


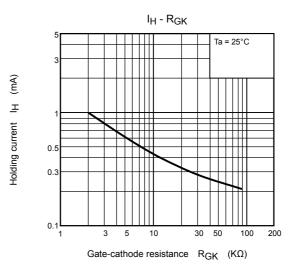












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